REMARKS

Claim 1 has been amended to call for a storage to store data from each of the processors so as to be selectively accessible by each of said processors. In other words, the storage provides a means of communication in which each of the processors can store data therein and each of the processors can access that data from the storage.
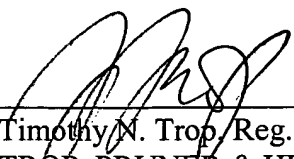
Claim 16 has been amended to call for enabling each of the processors to store data in a storage and to selectively access the data stored in said storage by another one of said processors. Such a means of communication is nowhere suggested in any of the cited references.

The argument that there are numerous storages that can be only accessed by one processor in each case in the prior art fails to meet the claimed limitation. The claim language is clear that there must be a storage that stores data from each of said processors so as to be selectively accessible by each of said processors. In the cited references there is no one storage that each of the processors can access, nor is there any way for other processors to access data stored by a different processor.

Therefore, reconsideration of the rejection is respectfully requested.

Respectfully submitted,

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